WHAT IS CLAIMED IS:

1. An apparatus, comprising:

a detector circuit to detect a processor type; and

a voltage provider circuit to provide a low-power state processor voltage in accordance with the processor type.

2. The apparatus of claim 1, wherein the detector circuit is to detect the processor type in accordance with a signal from a processor pin.

3. The apparatus of claim 2, wherein the detector circuit includes:

a transistor having a base to receive the signal from the processor pin, wherein the transistor is on when a first processor type is present and off when a second processor type is present.

4. The apparatus of claim 3, wherein the transistor comprises a bi-polar junction transistor and the collector of the transistor provides a processor type signal to the voltage provider circuit.

5. The apparatus of claim 1, wherein the voltage provider circuit provides a first voltage level when a first processor type is detected and a second voltage level when a second processor type is detected.

6. The apparatus of claim 5, wherein the voltage provider circuit includes:

a voltage divider.

7. The apparatus of claim 6, wherein the voltage provider circuit receives a processor type signal from the detector circuit and further includes:

a transistor to adjust a resistance associated with the voltage divider in accordance with the processor type signal.

8. The apparatus of claim 7, wherein the transistor comprises an n-channel inverter.

9. The apparatus of claim 1, further comprising:

an offset voltage circuit to adjust an offset value associated with a processor voltage in accordance with the processor type.

10. The apparatus of claim 1, wherein the detector circuit and the voltage provider circuit are associated with a voltage regulator integrated circuit.

11. An apparatus, comprising:

a detector circuit to detect a processor type; and

a voltage provider circuit to provide a processor voltage in accordance with the processor type.

12. The apparatus of claim 11, wherein a first voltage level is provided when a first processor type is detected and a second voltage level is provided when a second processor type is detected.

13. An apparatus, comprising:

an input to receive a signal associated with a processor type; and

an output to provide a low-power state processor voltage in accordance with the processor type.

14. The apparatus of claim 13, wherein a first voltage level is provided for a first processor type and a second voltage level is provided for a second processor type.

15. The apparatus of claim 13, wherein the input receives an input signal from a processor pin.

16. The apparatus of claim 13, wherein the output provides an output signal to a processor pin.

17. An apparatus, comprising:

a first transistor, including

a base to receive a signal from a processor pin, the first transistor being on when a first processor type is present and off when a second processor type is present, and

a collector to provide a processor type signal; and

a second transistor, including:

a gate to receive the processor type signal,

wherein the second transistor is to adjust a resistance associated with a voltage divider such that one of a first and a second low-power state processor voltage is provided from the voltage divider in accordance with the processor type signal.

18. The apparatus of claim 17, wherein the apparatus comprises a voltage regulator integrated circuit.

19. A method, comprising:

detecting a processor type; and

providing a low-power state processor voltage in accordance with the processor type.

20. The method of claim 19, wherein a first voltage level is provided when a first processor type is detected and a second voltage level is provided when a second processor type is detected.

21. The method of claim 19, further comprising:

adjusting an offset value in accordance with the processor type.

22. A system, comprising:

a power supply to convert alternating current power to direct current power; and

a voltage regulator coupled to the power supply and including:

a detector circuit to detect a processor type, and

a voltage provider circuit to provide a low-power state processor voltage in accordance with the processor type.

23. The system of claim 22, wherein a first voltage level is provided when a first processor type is detected and a second voltage level is provided when a second processor type is detected.